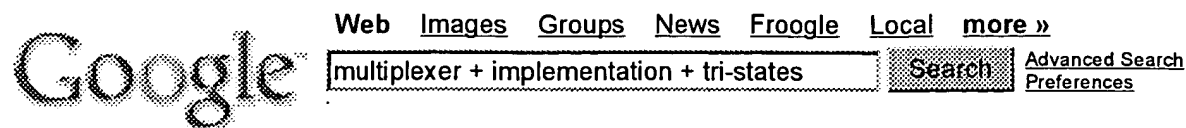


Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	472	(712/218).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/27 08:07
S1	9	"5805852"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:34
S2	24	"4591973"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/28 16:41
S3	2	((("5805852") or ("4591973"))).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/28 16:41
S4	0	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) near4 (multiplexer\$1 or selector\$1) near4 (data adj1 input\$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:36
S5	0	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) near4 (multiplexer\$1 or selector\$1) near4 (data near4 input\$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:37
S6	0	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) near4 (multiplexer\$1 or select\$3) near4 (data near4 input\$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:38
S7	1	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) with ((multiplexer\$1 or select\$3) near4 (data near4 input\$4))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:39
S8	130	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) same ((multiplexer\$1 or select\$3) near4 data)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:40
S9	8	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) near4((multiplexer\$1 or select\$3) near4 data)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:43
S10	25	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) with ((multiplexer\$1 or select\$3) near4 data)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:49
S11	72	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) with ((multiplexer\$1 or select\$3) near4 (execut\$3 or process\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:50

S12	58	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) near4 ((multiplexer\$1 or select\$3) near4 (execut\$3 or process\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:52
S13	12	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) near4 (multiplexer\$1 near4 (execut\$3 or process\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:57
S14	12	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) near4 (multiplex\$3 near4 (execut\$3 or process\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 15:00
S15	51	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) near4 (select\$3 near4 (execut\$3 or process\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 14:59
S16	17	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) with (multiplex\$3 near4 (execut\$3 or process\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 15:02
S17	32	(vliw or (very adj1 long adj1 instruction adj1 word\$1)) same (multiplex\$3 near4 (execut\$3 or process\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/04 15:04
S18	445	(712/218).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/05 15:27

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implementation of the proposed RSA processor on a COTS ... significant bit of a word, and an S-wide **multiplexer**, indicating whether to execute the shift ...doi.ieeecomputersociety.org/10.1109/DATE.2003.10188 - [Similar pages](#)[PDF] Xilinx Virtex Devices: Variable Input LUT Architecture New Stratix ...File Format: PDF/Adobe Acrobat - [View as HTML](#)in Figure 1 is the **implementation** of an 8:1 and a 16:1 **multiplexer** in a ...converts tristate drivers to muxes when the number of **tristates** drivers on the ...www.synplicity.com/literature/ syndicated/pdf/v4_i1/syndicated_v4_i1.pdf - [Similar pages](#)[PDF] Microsoft PowerPoint - CMOS circuits 2004File Format: PDF/Adobe Acrobat - [View as HTML](#)**Tristates**. • Tristate buffer produces Z when not enabled ... 4:1 **Multiplexer**.

• 4:1 mux chooses one of 4 inputs using two selects. EE 261. Krish Chakrabarty ...

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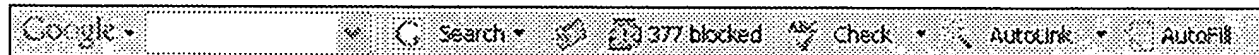
Multiplexer: 8-10 LEs Page 38. ... Synthesis Assumes **Tri-States** are One-Hot 38 ...

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